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BK Bray, MJ Flynn - Proceedings of the 24th annual international symposium on ...., 1991 - portal.acm.org
... **prediction** performance, we observed the **branching** characteristics of ... unit of
instructions, hence closely spaced **branches** could ... A **branch** can miss in the btb and ...
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The Performance of Counter-and Correlation-Based Schemes for **Branch Target Buffers** - group of 8 »
A Mital - IEEE Transactions on Computers, 1995 - doi.ieeecs.org
... custom tools to extract all **branching** information. ... 7. Counter-based **branch prediction**
combined with caching ... counters with **predicting** negative **branches** as taken ...
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M Moudgill, K Pingali, S Vassiliadis - 1993 - IEEE Computer Society Press Los Alamitos, CA, USA
... if it is later determined that control **branched** to LI . ... **Branches** are mispredicted
fairly often. The mechanism that recovers from a mispredicted **branch** must be ...
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[BOOK] Representative Traces for Processor Models with Infinite Cache - group of 4 »
VS Iyengar, L Trevillyan, P Bose - 1995 - IBM TJ Watson Research Center
... **Branches** are particularly inter- esting because of the ... O) met- ric that ignores the
branch prediction part of ... indicate the impact of the **branching** history part ...
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Exploiting Fine-Grained Parallelism Through a Combination of Hardware and Software Techniques - group of 3 »
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... However, if code which previously **branched** to A can ... t do branchault **prediction**, only
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Representative Traces For
VS Iyengar, LH Trevillyan, P Bose - ieeexplore.ieee.org
... **Branches** are particularly inter- esting because of the impact ... than that using the
exact **branch prediction** scheme and ... is quali- fied by the **branching** history of ...
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[BOOK] Achieving High Levels of Instruction-level Parallelism with Reduced Hardware Complexity - group of 6 »
M Schlansker... - 1996 - hpl.hp.com
... Predicated execution Dynamic **branch prediction** ... compile-time motion of code above
conditional **branches**, in order to make run-time ... memory, and **branch** units. ...
Cited by 34 - Related Articles - View as HTML - Web Search - Library Search

Performance enhancement through dynamic scheduling and large execution atomic units in single ...
SW Melvin - 1990 - zytek.com
... that are easy to **predict** statically, for example loops ... dynamic **branch prediction**
to allow this parallelism to be taken ... root of the number of **branches** that are ...
Cited by 1 - Related Articles - View as HTML - Web Search - Library Search

Exploiting program **branch** probabilities in hardware compilation - group of 5 »
H Styles, W Luk - IEEE Transactions on Computers, 2004 - doi.ieeecomputersociety.org
... Designs sustain throughput for all **branching** probabilities. ... combine loop carry
dependencies and biased **branches**. ... system in which **branch** optimization techniques ...
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Comparative Architectures - group of 2 »
I Pratt - 1998 - cl.cam.ac.uk
... Load/**branch** delay slots ... Historically, the **predictions** made by this document have
been surpassed by the ... 7. people have repeatedly **predicted** an end to line size ...
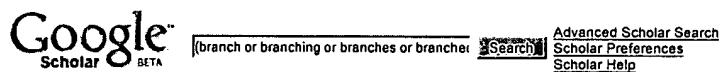Cited by 1 - Related Articles - View as HTML - Web Search

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TH Meng - 1991 - books.google.com
... **buffer** schemes and usually results in a much higher design cost than a ... **"Prediction**
of Error Probability for Integrated Digital Synchronizers," IEEE J. on SoM ...
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[DOC] <u>Deliverable reference number: D. WP. JRA. 3.2. 1 Deliverable title:" State of the Art in Resource ...</u>
E MSPhotoEd, SF Programme - eurongi.enst.fr
... SCR allocation is adequate under the infinite **buffering** assumption. However, delay
can be significant when **buffer** size is too large and the **buffers** cannot be ...
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